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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/881,974 06/15/2001 PAT 6,759,115

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-180086 06/15/2000

JAPAN 2000-206262 07/07/2000

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 06/17/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met	STATE OR COUNTRY JAPAN	SHEETS DRAWING 3	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 1
Verified and Acknowledged <i>Cja</i> Examiner's Signature	Allowance Initials				

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## TITLE

Multilayer circuit component and method for manufacturing the same

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